



2186

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: William P. Ward

Serial No.: 09/651,229

Filed: August 25, 2002

For: Providing Multiple Memory  
Controllers on a Memory Bus

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Group Art Unit: 2186

Examiner: P.M. Bataille

Atty. Dkt. No. 9295 (NCR.0020US)

17/E<sup>NE</sup>  
C. B.  
2/20/04

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313

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Technology Center 2100

REPLY TO OFFICE ACTION DATED DECEMBER 9, 2003

Dear Sir:

In response to the Office Action dated December 9, 2003, Applicant submits the following remarks regarding the above-identified application.

The claims have not been amended by this paper.

**Remarks/Arguments** begin on page 9 of this paper.

Date of Deposit:	2/4/2004
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313.	
Dawn L. Thomas	
Dawn L. Thomas	